# Jutge.org

The Virtual Learning Environment for Computer Programming

## Full adder

Design a **full adder**. A full adder is a circuit that performs a 1-bit addition receiving an input carry and generating an output carry.

### Specification

```
module full_adder(a, b, cin, sum, cout);
input a, b, cin;
output sum, cout;
```

#### Input

- *a* and *b* are the two inputs.
- **cin** is the input carry.

#### Output

- *sum* is the sum of the two bits and the input carry.
- **cout** is the output carry.

#### **Problem information**

Author : Jordi Cortadella Generation : 2025-02-09 12:27:59

© *Jutge.org*, 2006–2025. https://jutge.org