Mod-3 counter   X26847_en

Design a circuit that implements a mod-3 counter, i.e., it outputs the sequence $0 \rightarrow 1 \rightarrow 2 \rightarrow 0 \rightarrow 1 \rightarrow 2 \rightarrow \cdots$.

Specification

> **module** *mod3_counter*(*count*, *clk*, *rst*);
>   **input** *clk* , *rst* ;
>   **output** [1:0] *count*;

Input

- *clk* is the clock signal.

- *rst* is the synchronous reset signal.

Output

- *count* is the 2-bit output of the counter.

Problem information

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