
Bus invert

X29475_en

Design a circuit that inverts the value of an N -bit bus. N must be a parameter of the circuit with default value 8.

Specification

```
module bus_invert(in, out);  
    parameter N=8;  
    input [N-1:0] in;  
    output [N-1:0] out;
```

Input

- in is the N -bit input bus.

Output

- out is the N -bit inverted value of the bus.

Problem information

Author : Jordi Cortadella

Generation : 2013-09-02 15:58:31

© Jutge.org, 2006–2013.

<http://www.jutge.org>