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**Majority****X70559\_en**

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Design a circuit that implements the 5-input majority function, that is, the output is 1 when there are more ones than zeros at the inputs.

The top module must be called *majority*.

```
module majority(a, b, c, d, e, maj);
    input a, b, c, d, e;
    output maj;
```

**Input**

- $a, b, c, d$  and  $e$  are the inputs.

**Output**

- $maj$  is the output implementing the majority function.

**Problem information**

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